

Search Notes 	Application/Control No.	Applicant(s)/Patent under Reexamination	
	10/771,466	PREISACH, HELMUT	
	Examiner Howard L. Williams	Art Unit 2819	Search Notes Page 1 of 2

SEARCHED

INTERFERENCE SEARCHED			
Class	Subclass	Date	Examiner

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

341/141

1. (Original) Digital to analog converter, wherein the digital to analog converter is implemented by two or more digital to analog converters, in that it is furthermore implemented in such a way that the operation of its analog output voltages is combined and in that its digital input values are processed in such a way that, in the case of a continuous incrementing or decrementing of said input values, the individual input values of the two or more digital to analog converters are incremented or decremented in turn.

Claims
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2. (Original) Phase control circuit, comprising a phase detector, a control-circuit filter and a voltage-controlled oscillator, in which the phase detector is constructed in such a way that it compares the phase of the output signal of the voltage-controlled oscillator with the phase of an input signal and delivers an error signal that corresponds to the difference between the two phases, and in which the control-circuit filter is constructed in such a way that it converts the error signal into a control signal for the voltage-controlled oscillator in such a way that the latter is able to follow both the dynamic fluctuations of the phase of the input signal and also long-term variations in this phase, wherein the control-circuit filter comprises two parallel branches of which a first branch is dimensioned with regard to the dynamic fluctuations and a second branch is dimensioned with regard to the long-term variations.

Above note
by T.
Cunningham
2/23/05